PATENT APPLICATION

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200301728-2	APPLICATION NO.	CONFIRMATION NO.
	APPLICANT William P. BUNTON t al.		
	FILING DATE Her with	GROUP	

REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
QG	1A	4,984,251	01/1991	Perloff et al.	Previously cited in parent case Serial No. 09/596,980.
QG	1B	6,385,671	05/2002	Hunsaker et al.	Previously cited in parent case Serial No. 09/596,980.
QG	1C	6,512,804	01/2003	Johnson et al.	Previously cited in parent case Serial No. 09/596,980.
QG	1D	5,020,023	05/1991	Smith, Thomas B.	Previously cited in parent case Serial No. 09/596,980.
QG	1E	6,459,746	10/2002	Agazzi et al.	Previously cited in parent case Serial No. 09/596,980.
QG	1F	5,835,498	11/1998	Kim et al.	Previously cited in parent case Serial No. 09/596,980.
	1G				
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		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

QG	1Q	Patent Application Serial No. 09/597,192, filed June 20, 2000, and entitled "High-Speed Interconnection Adapter Having Automated Crossed Differential Pair Correction" by William P. Bunton, John Krause and Patricia L. Whiteside.
QG	1R	Patent Application Serial No. 09/597,190, filed June 20, 2000, and entitled "High-Speed Interconnection Link Having Automated Lane Reordering" by William P. Bunton, John Krause and Patricia L. Whiteside.
QG	1S	"A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," IBM J. Res. Develop., Vol. 27 No. 5, September 1983 by A. X. Widmer and P.A. Franaszek, pp. 440-451, Tables (6 p.).

EXAMINER	/Qutbuddin Ghulamali/	DATE CONSIDERED	12/01/2006

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EXAMINER	/Qutbuddin Ghulamali/	DATE CONSIDERED	12/01/2006